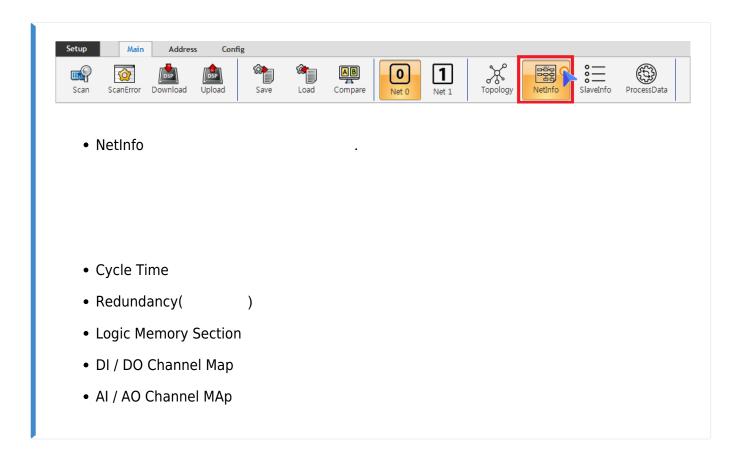
2025/10/08 20:28 1/1 Net Info

| Net Info | 1 |
|----------------------|---|
| | 1 |
| Info | |
| Logic Memory Section | |
| Channel Map | |

2025/10/08 20:28 1/4 Net Info

Net Info

× Configurator Net Information Tab



Info Info Logic Out | Logic In | DI/O Channel Map | AI/O Channel Map | FMMU | Driver PDO Cvcle Time 1000 Cycle Time μs (Min: 10μs / Max: 1000μs) 60 Process Time (Net) Cycle time of the DSP-Core0 36 ProcessTime(App) Cycle time of the DSP-Core 1 Data Size 27 Byte Output 51 Input Byte DC Setup 50000 Dc Offset Redundance ☐ Redundancy (링 이중화) 사용

2025/10/08 20:28 2/4 Net Info

Cycle Time

```
• Cycle Time

    Download

         가
                  125 / 250 / 500 / 1000 / 2000 / 4000
                                                                 us
                         <sup>1)</sup>, 500us
        · 32
                                      가
        ∘ 16
                         , 250us
                                      가
        ∘ 8
                        , 125us
                                     가
× ProcessTime CycleTime
   • Process Time
        ∘ Process Time (Net) : DSP-Core 0
        ∘ Process Time (App) : DSP-Core 1
        Process Time
                              us
Data Size

    OutPut : Output Data

                                   . 2048 Byte

    Input : Input Data

                               . 2048 Byte
DC Setup
   • SyncMode가 DC-Sync
                   EtherCAT Frame
        Master
   • Frame
         가
                      가
                                                       가
                                         가
             EtherCAT Frame

    Master

                DC Offset
                        50,000 50us
           ns
               -999,999 ~ 999,999
                  50,000 ~ 100,000
                                               가
              가
                    , -50,000
    (-)
                                                    50us
                                                               Frame
                                                              , DC-Offset
        Frame
                                 가
                                         DC Offset
                                                    50,000
                                                                     50us
```

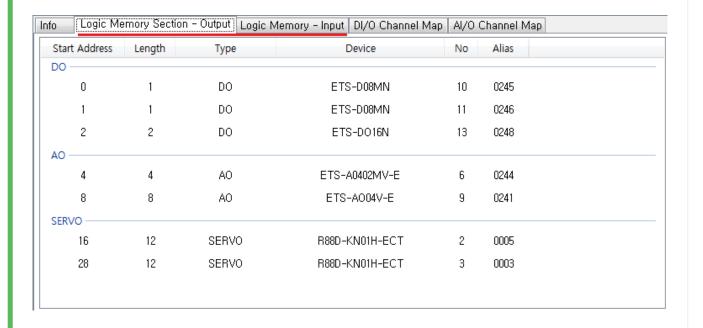
, DC-Offset

2025/10/08 20:28 3/4 Net Info

Redundancy

• , "Redundancy() "

Logic Memory Section

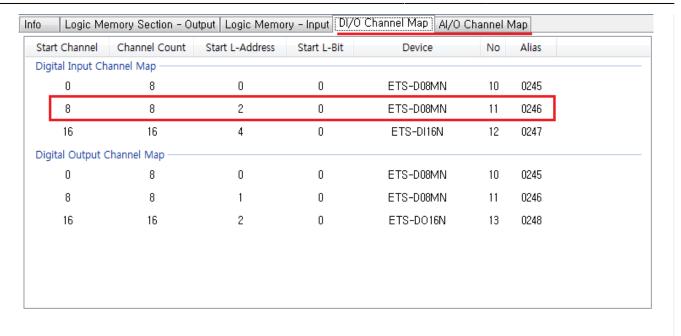


• (Input/Output) Slave가 .

o Master Slave Frame .

Channel Map

2025/10/08 20:28 4/4 Net Info



• Slave Type DI / DO / AI / AO Slave Global Channel Map

∘ ex) 0x246 ETS-D08MN

8 ~ 15 Global Channel

• Channel Map

IO Channel Mapping

1)

COMI-LX554 64

From:

http://comizoa.co.kr/info/ - -

Permanent link:

 $http://comizoa.co.kr/info/doku.php?id=platform:ethercat:1_setup:10_config:20_desc:20_netinfo\&rev=1671090221$

Last update: 2024/07/08 18:22