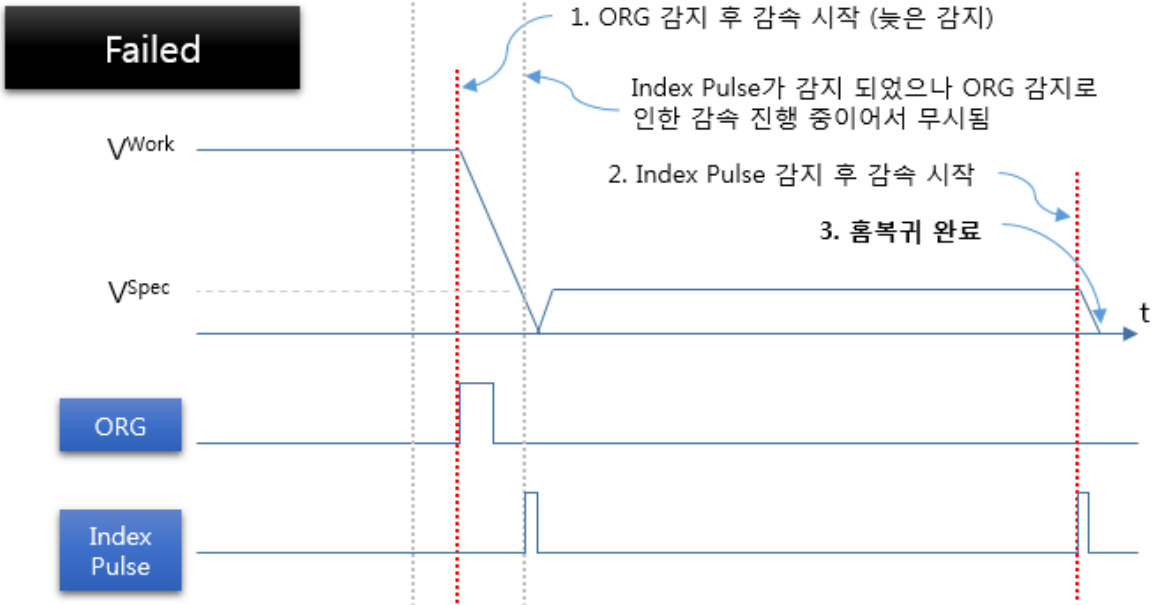
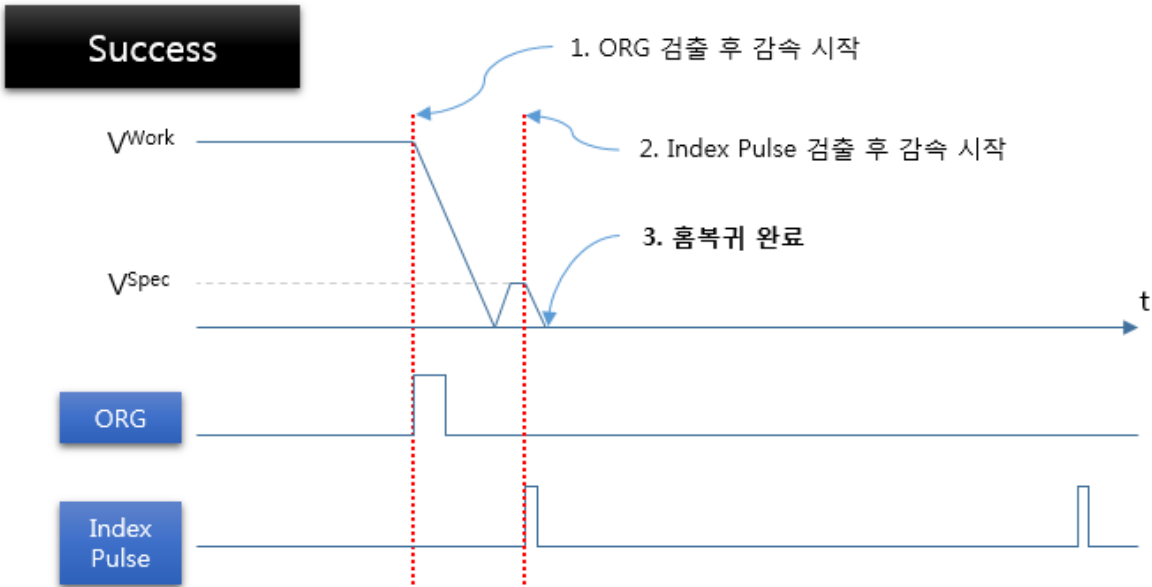


Case 1	1
AddInfo	1
Cause	1
Solution	2
Case 2	3
AddInfo	3
Cause	3
Solution	4

× 가 .

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- Index Pulse , 1 (ORG)가 index Pulse , Index Pulse 1

Solution

- Index Pulse 1 1
- (Vwork)

Case 2

AddInfo

• 100

homeMode

Index Pulse(Z)

Mode

Cause

• 100

homeMode

가 .

• Index Pulse

PDO()

.

•

Z

,

Index Pulse

.

The diagram illustrates a speed control scenario. The top graph shows speed V_{Work} and V_{Spec} over time t . V_{Work} starts at a high level, then drops to V_{Spec} when the ORG signal is triggered. It remains at V_{Spec} until the Index Pulse is received, after which it drops again. Two vertical dashed lines mark the '예상 완료 위치' (Expected completion position) and '실제 완료 위치' (Actual completion position). The actual completion position is later than the expected one. Below the speed graph, three signal waveforms are shown: ORG (a single pulse), PDO Transfer (a series of pulses), and Index Pulse (a single pulse). Annotations explain the timing: 1. ORG 검출 후 감속 시작 (Deceleration starts after ORG detection), Index Pulse가 검출 되었으나 PDO Transfer 시점에 전달되지 않음 (Index Pulse was detected but not transmitted at PDO Transfer time), and 2. PDO Transfer 시점에 IndexPulse가 검출되어 마스터로 검출 여부 전달 (At PDO Transfer time, IndexPulse was detected and the detection result was transmitted to the master).

•

Index Pulse 2

.

Solution

- 가
- Index Pulse ()
- Index Pulse Latch
- Index Pulse (Vspec) (Index Pulse)

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