0. ETS-AI08H-E R	ange Mode Set	ting1
1. ComilDE Dictio	onary	
1. ComilDE Die	ctionary	
2.	,	
3. Range		2
2. SDO Write		
1. Slave		
2.		
3 SDO Write	Range Mode	5
51 50 6 111100	Range Floae	

ETS-AI08H-E Range Mode

 \times ETS-AI08H-E Range Mode

0. ETS-AI08H-E Range Mode Setting

▶ Analog Input Channel Setting (n = 0~7 : 채널 번호)

Index	Sub	Name		Value	Data Type	Flag	
	0x00	AI Setting Ch. n		0x0B	UINT8	RO	
	0x01	Enable Limit 1		Limit 1 사용 여부	BOOL	RW	
	0x02	Enable Limit 2		Limit 2 사용 여부	BOOL	RW	
	0~02		0	Voltage Type	POOL	DW	
	0x05	Analog Type	1	Current Type	BOOL	KVV	
			0	-10.24 ~ 10.24 (V)			
			1	-5.12 ~ 5.12 (V)			
0x80n0				2	-2.56 ~ 2.56 (V)		
	0.00	Danga Mada	3	0 ~ 10.24 (V)	DITE	DW	
	0x09	Range Mode	4	0 ~ 5.12 (V)	CIID	RVV	
			5	4 ~ 20 (mA)			
			6	0 ~ 20 (mA)			
			7	0 ~ 24 (mA)			
	0x0A	Limit1 Value		Limit 1 값	INT32	RW	
	0x0B	Limit2 Value		Limit 2 값	INT32	RW	

1. ComilDE Dictionary



2.

]	Index	N 1						
	IIIdex	Name	Туре	Size	Value	Value_Hex	Flag	1
	0x1000	Device type	UDINT		791433	0xC1389		
	0×1008	Device name	STRING(10)	10	AES-CBC-AI			1
	0×1009	Hardware version	STRING(4)	4	v1.0			
	0×100A	Software version	STRING(8)	8	v1.0.0.0			
÷	0×1018	Identity	DT1018	18				
÷	0×1600	AO R×PDO-Map	DT1600	50				
÷	0×1A00	AI TxPDO-Map00	DT1A00	38				
÷	0×1A01	AI TxPDO-Map01	DT1A00	38				
÷	0x1A02	AI T×PDO-Map02	DT1A00	38				
÷	0x1A03	AI TxPDO-Map03	DT1A00	38				
÷	0x1A04	AI T×PDO-Map04	DT1A00	38				
÷	0x1A05	AI T×PDO-Map05	DT1A00	38				
÷	0×1A06	AI T×PDO-Map06	DT1A00	38				
÷	0×1A07	AI TxPDO-Map07	DT1A00	38				
÷	0×1A08	AI T×PDO-Map08	DT1A00	38				
÷	0×1A09	AI T×PDO-Map09	DT1A00	38				
÷	0×1A0A	AI TxPDO-Map10	DT1A00	38				
÷	0×1A0B	AI TxPDO-Map11	DT1A00	38				
÷	0x1A20	AI(SOL) TxPDO-Map00	DT1A00	38				
÷	0x1A21	AI(SOL) TxPDO-Map01	DT1A00	38				
÷	0x1A22	AI(SOL) TxPDO-Map02	DT1A00	38				
	001623	AI/SOLE TOPDO_Mon03	DT1A00	20				1
		0x1000 0x1008 0x1009 0x100A 0x100A 0x100A 0x100A 0x100A 0x100A 0x100B 0x100C 0x1A00 0x1A00 0x1A00 0x1A01 0x1A02 0x1A03 0x1A03 0x1A04 0x1A05 0x1A06 0x1A07 0x1A08 0x1A09 0x1A0A 0x1A0A 0x1A0A 0x1A0A 0x1A0A 0x1A0A 0x1A0A 0x1A0A 0x1A0A 0x1A20 0x1A21 0x1A22	0x1000 Device name 0x1003 Device name 0x1009 Hardware version 0x100A Software version 0x100A Software version 0x100A Software version 0x100A Software version 0x100A AO RxPDO-Map 0x1A00 AI TxPDO-Map00 0x1A01 AI TxPDO-Map01 0x1A02 AI TxPDO-Map02 0x1A03 AI TxPDO-Map03 0x1A04 AI TxPDO-Map03 0x1A05 AI TxPDO-Map05 0x1A06 AI TxPDO-Map06 0x1A07 AI TxPDO-Map07 0x1A08 AI TxPDO-Map07 0x1A08 AI TxPDO-Map08 0x1A09 AI TxPDO-Map08 0x1A08 AI TxPDO-Map09 0x1A08 AI TxPDO-Map01 0x1A08 AI TxPDO-Map01 0x1A08 AI TxPDO-Map10 0x1A09 AI TxPDO-Map11 0x1A02 AI(SOL) TxPDO-Map01 0x1A21 AI(SOL) TxPDO-Map01 0x1A22 AI(SOL) TxPDO-Map02 </td <td>0x1000 Device type 0x101 0x1008 Device name STRING(10) 0x1009 Hardware version STRING(4) 0x100A Software version STRING(8) 0x100A Software version STRING(8) 0x100A Software version STRING(8) 0x100A Software version STRING(8) 0x100A AS RxPDO-Map DT100 0x1400 AI TxPDO-Map0 DT1A00 0x1A01 AI TxPDO-Map02 DT1A00 0x1A02 AI TxPDO-Map02 DT1A00 0x1A03 AI TxPDO-Map03 DT1A00 0x1A03 AI TxPDO-Map04 DT1A00 0x1A04 AI TxPDO-Map05 DT1A00 0x1A05 AI TxPDO-Map05 DT1A00 0x1A06 AI TxPDO-Map07 DT1A00 0x1A08 AI TxPDO-Map09 DT1A00 0x1A08 AI TxPDO-Map09 DT1A00 0x1A08 AI TxPDO-Map09 DT1A00 0x1A08 AI TxPDO-Map10 DT1A00 0x1A08 A</td> <td>0x1000 Device name STRING(10) 10 0x1008 Device name STRING(10) 10 0x1009 Hardware version STRING(4) 4 0x100A Software version STRING(8) 8 0x100A Software version STRING(8) 8 0x100A Software version STRING(8) 8 0x100A AO RxPDO-Map DT1018 18 0x1600 AO RxPDO-Map DT1600 50 0x1A00 AI TxPDO-Map00 DT1A00 38 0x1A01 AI TxPDO-Map01 DT1A00 38 0x1A02 AI TxPDO-Map02 DT1A00 38 0x1A03 AI TxPDO-Map03 DT1A00 38 0x1A04 AI TxPDO-Map05 DT1A00 38 0x1A05 AI TxPDO-Map05 DT1A00 38 0x1A06 AI TxPDO-Map07 DT1A00 38 0x1A08 AI TxPDO-Map08 DT1A00 38 0x1A08 AI TxPDO-Map09 DT1A00 38 <tr< td=""><td>0x1000 0x1000 0x1000<</td><td>0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1009 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x1600 AO RxPDO-Map DT1600 50 0x1A00 AI TxPDO-Map00 DT1A00 38 0x1A02 AI TxPDO-Map01 DT1A00 38 0x1A03 AI TxPDO-Map02 DT1A00 38 0x1A04 AI TxPDO-Map03 DT1A00 38 0x1A05 AI TxPDO-Map05 DT1A00 38 0x1A06 AI TxPDO-Map07 DT1A00 38 0x1A08 AI TxPDO-Map08 DT1A00 38 0x1A08 AI TxPDO-Map09 DT1A00 38 0x1A08 AI TxPDO-Map10 DT1A00<td>0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1003 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0.0.0 0x1005 AD RxPD0-Map DT1018 18 0x1600 AO RxPD0-Map DT1600 50 0x1401 AI TxPD0-Map00 DT1A00 38 0x1A02 AI TxPD0-Map02 DT1A00 38 0x1A03 AI TxPD0-Map03 DT1A00 38 0x1A04 AI TxPD0-Map05 DT1A00 38 0x1A05 AI TxPD0-Map06 DT1A00 38 0x1A06 AI TxPD0-Map07 DT1A00 38 0x1A08 AI TxPD0-Map09 DT1A00 38 0x1A08 AI TxPD0-Map010 DT1A00</td></td></tr<></td>	0x1000 Device type 0x101 0x1008 Device name STRING(10) 0x1009 Hardware version STRING(4) 0x100A Software version STRING(8) 0x100A Software version STRING(8) 0x100A Software version STRING(8) 0x100A Software version STRING(8) 0x100A AS RxPDO-Map DT100 0x1400 AI TxPDO-Map0 DT1A00 0x1A01 AI TxPDO-Map02 DT1A00 0x1A02 AI TxPDO-Map02 DT1A00 0x1A03 AI TxPDO-Map03 DT1A00 0x1A03 AI TxPDO-Map04 DT1A00 0x1A04 AI TxPDO-Map05 DT1A00 0x1A05 AI TxPDO-Map05 DT1A00 0x1A06 AI TxPDO-Map07 DT1A00 0x1A08 AI TxPDO-Map09 DT1A00 0x1A08 AI TxPDO-Map09 DT1A00 0x1A08 AI TxPDO-Map09 DT1A00 0x1A08 AI TxPDO-Map10 DT1A00 0x1A08 A	0x1000 Device name STRING(10) 10 0x1008 Device name STRING(10) 10 0x1009 Hardware version STRING(4) 4 0x100A Software version STRING(8) 8 0x100A Software version STRING(8) 8 0x100A Software version STRING(8) 8 0x100A AO RxPDO-Map DT1018 18 0x1600 AO RxPDO-Map DT1600 50 0x1A00 AI TxPDO-Map00 DT1A00 38 0x1A01 AI TxPDO-Map01 DT1A00 38 0x1A02 AI TxPDO-Map02 DT1A00 38 0x1A03 AI TxPDO-Map03 DT1A00 38 0x1A04 AI TxPDO-Map05 DT1A00 38 0x1A05 AI TxPDO-Map05 DT1A00 38 0x1A06 AI TxPDO-Map07 DT1A00 38 0x1A08 AI TxPDO-Map08 DT1A00 38 0x1A08 AI TxPDO-Map09 DT1A00 38 <tr< td=""><td>0x1000 0x1000 0x1000<</td><td>0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1009 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x1600 AO RxPDO-Map DT1600 50 0x1A00 AI TxPDO-Map00 DT1A00 38 0x1A02 AI TxPDO-Map01 DT1A00 38 0x1A03 AI TxPDO-Map02 DT1A00 38 0x1A04 AI TxPDO-Map03 DT1A00 38 0x1A05 AI TxPDO-Map05 DT1A00 38 0x1A06 AI TxPDO-Map07 DT1A00 38 0x1A08 AI TxPDO-Map08 DT1A00 38 0x1A08 AI TxPDO-Map09 DT1A00 38 0x1A08 AI TxPDO-Map10 DT1A00<td>0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1003 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0.0.0 0x1005 AD RxPD0-Map DT1018 18 0x1600 AO RxPD0-Map DT1600 50 0x1401 AI TxPD0-Map00 DT1A00 38 0x1A02 AI TxPD0-Map02 DT1A00 38 0x1A03 AI TxPD0-Map03 DT1A00 38 0x1A04 AI TxPD0-Map05 DT1A00 38 0x1A05 AI TxPD0-Map06 DT1A00 38 0x1A06 AI TxPD0-Map07 DT1A00 38 0x1A08 AI TxPD0-Map09 DT1A00 38 0x1A08 AI TxPD0-Map010 DT1A00</td></td></tr<>	0x1000 0x1000<	0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1009 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x100A Software version STRING(8) 8 v1.0,0,0 0x1600 AO RxPDO-Map DT1600 50 0x1A00 AI TxPDO-Map00 DT1A00 38 0x1A02 AI TxPDO-Map01 DT1A00 38 0x1A03 AI TxPDO-Map02 DT1A00 38 0x1A04 AI TxPDO-Map03 DT1A00 38 0x1A05 AI TxPDO-Map05 DT1A00 38 0x1A06 AI TxPDO-Map07 DT1A00 38 0x1A08 AI TxPDO-Map08 DT1A00 38 0x1A08 AI TxPDO-Map09 DT1A00 38 0x1A08 AI TxPDO-Map10 DT1A00 <td>0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1003 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0.0.0 0x1005 AD RxPD0-Map DT1018 18 0x1600 AO RxPD0-Map DT1600 50 0x1401 AI TxPD0-Map00 DT1A00 38 0x1A02 AI TxPD0-Map02 DT1A00 38 0x1A03 AI TxPD0-Map03 DT1A00 38 0x1A04 AI TxPD0-Map05 DT1A00 38 0x1A05 AI TxPD0-Map06 DT1A00 38 0x1A06 AI TxPD0-Map07 DT1A00 38 0x1A08 AI TxPD0-Map09 DT1A00 38 0x1A08 AI TxPD0-Map010 DT1A00</td>	0x1000 Device name STRING(10) 10 AES-CBC-AI 0x1003 Hardware version STRING(4) 4 v1.0 0x1004 Software version STRING(8) 8 v1.0.0.0 0x1005 AD RxPD0-Map DT1018 18 0x1600 AO RxPD0-Map DT1600 50 0x1401 AI TxPD0-Map00 DT1A00 38 0x1A02 AI TxPD0-Map02 DT1A00 38 0x1A03 AI TxPD0-Map03 DT1A00 38 0x1A04 AI TxPD0-Map05 DT1A00 38 0x1A05 AI TxPD0-Map06 DT1A00 38 0x1A06 AI TxPD0-Map07 DT1A00 38 0x1A08 AI TxPD0-Map09 DT1A00 38 0x1A08 AI TxPD0-Map010 DT1A00

3. Range

02 : [0202] AES-CBC-AIO			Update Ir	nterval 1000			Co	nfig	
EtherCAT	1	Index	Name	Туре	Size	Value	Value_Hex	Flag	٦
	+	0x7210	Analog(SOL) Output Channel 1	DT7000	4				
	÷	0×7220	Analog(SOL) Output Channel 2	DT7000	4				
02 : [0202] AES-CBC-AIO	÷	0×7230	Analog(SOL) Output Channel 3	DT7000	4				
	÷	0×7240	Analog(SOL) Output Channel 4	DT7000	4				
	÷	0×7250	Analog(SOL) Output Channel 5	DT7000	4				
	÷	0×7260	Analog(SOL) Output Channel 6	DT7000	4				
	÷	0x7270	Analog(SOL) Output Channel 7	DT7000	4				
		0×8000	Analog Input Setting Ch.0	DT8000	24				
		0x0	SubIndex 000	USINT	1	11	0×B		
		0×1	Enable Limit1	BOOL	0	0	0×0		
		0x2	Enable Limit2	BOOL	0	0	0×0		
		0x9	Range Mode	DT8001EN	1	0	0x0		
		UxA	Limit1 Value	DINT	4	0	0×0		
		0×B	Limit2 Value	DINT	4	0	0×0		
	÷	0×8010	Analog Input Setting Ch,1	DT8000	24				
	÷	0×8020	Analog Input Setting Ch,2	DT8000	24				
	÷	0×8030	Analog Input Setting Ch,3	DT8000	24				
	÷	0×8040	Analog Input Setting Ch,4	DT8000	24				
	÷	0×8050	Analog Input Setting Ch,5	DT8000	24				
	÷	0×8060	Analog Input Setting Ch,6	DT8000	24				
Index Order ID Order	÷	0×8070	Analog Input Setting Ch,7	DT8000	24				
		00,9090	Applea Input Sotting Ch 8	D T 2000	94				

Object Dictionary										x
02 : [0202] AES-CBC-AIO			Update Int	terval 1000				Co	nfig	
■ EtherCAT		Index	Name	Туре	Size	Value	Value_	Hex	Flag	^
	÷	0×7210	Analog(SOL) Output Channel 1	DT7000	4					1
	÷	0×7220	Analog(SOL) Output Channel 2	DT7000	4					
02 : [0202] AES-CBC-AIO	÷	0×7230	Analog(SOL) Output Channel 3	DT7000	4					
	÷	0×7240	Analog(SOL) Output Channel 4	DT7000	4					
	÷	0×7250	Analog(SOL) Output Channel 5	DT7000	4					
	÷	0×7260	Analog(SOL) Output Channel 6	DT7000	4					
	÷	0×7270	Analog(SOL) Output Channel 7	DT7000	4					
	-	0×8000	Analog Input Setting Ch.0	DT8000	24					
		0x0	SubIndex 000	USINT	1	11		0×B		
		0x1	Enable Limit1	BOOL	0	0		0×0		
		0x2	Enable Limit2	BOOL	0	0	_	0×0		
		0x9	Range Mode	DT8001EN	1	0		0x0		
		0xA	Limit1 Value	DINT	4			0×0		
		0xB	Limit2 Value	DINT	4	0		0×0		
	÷	0×8010	Analog Input Setting Ch, 1	DT8000	24					
	÷	0×8020	Analog Input Setting Ch.2	DT8000	24					
	÷	0×8030	Analog Input Setting Ch.3	DT8000	24					
	÷	0×8040	Analog Input Setting Ch.4	DT8000	24					
	÷	0×8050	Analog Input Setting Ch.5	DT8000	24					
	÷	0×8060	Analog Input Setting Ch.6	DT8000	24					
Index Order	÷	0×8070	Analog Input Setting Ch, 7	DT8000	24					
		008080	Applea Input Setting Ch 8	D T 2000	24					v

• Value

Object Dictionary									x
02 : [0202] AES-CBC-AIO]		Update Ir	nterval 1000			C	onfig	
<u> </u>		Index	Name	Туре	Size	Value	Value_Hex	Flag	^
	÷	0x7210	Analog(SOL) Output Channel 1	DT7000	4				-
	÷	0×7220	Analog(SOL) Output Channel 2	DT7000	4				
02 : [0202] AES-CBC-AIO	÷	0×7230	Analog(SOL) Output Channel 3	DT7000	4				
	÷	0×7240	Analog(SOL) Output Channel 4	DT7000	4				
	÷	0×7250	Analog(SOL) Output Channel 5	DT7000	4				
	÷	0×7260	Analog(SOL) Output Channel 6	DT7000	4				
	±	0×7270	Analog(SOL) Output Channel 7	DT7000	4				
		0×8000	Analog Input Setting Ch.0	DT8000	24				
		0×0	SubIndex 000	USINT	1	11	0×E	1	
		0×1	Enable Limit1	BOOL	0	0	0×0)	
		0x2	Enable Limit2	BOOL	0		1 0x0)	_
		0×9	Range Mode	DT8001EN	1	6	0x0)	
		0xA	Limit1 Value	DINT	4	0) 0x0)	
		0×B	Limit2 Value	DINT	4	0	0×0)	
	÷	0×8010	Analog Input Setting Ch.1	DT8000	24				
	÷	0×8020	Analog Input Setting Ch.2	DT8000	24				
	±	0×8030	Analog Input Setting Ch.3	DT8000	24				
	±	0×8040	Analog Input Setting Ch.4	DT8000	24				
	±	0×8050	Analog Input Setting Ch,5	DT8000	24				
	±	0×8060	Analog Input Setting Ch.6	DT8000	24				
Index Order	±	0×8070	Analog Input Setting Ch,7	DT8000	24				
Index order	÷	0×8080	Analog Input Setting Ch.8	DT8000	24				¥

Enter

U2 : [U2U2] AES-CBC-AIU			Update II	nterval 1000			Lor	nfig	_
⊒- EtherCAT		Index	Name	Туре	Size	Value	Value_Hex	Flag	1
	÷	0x7210	Analog(SOL) Output Channel 1	DT7000	4				
	÷	0x7220	Analog(SOL) Output Channel 2	DT7000	4				
02 : [0202] AES-CBC-AIO	÷	0x7230	Analog(SOL) Output Channel 3	DT7000	4				
	÷	0x7240	Analog(SOL) Output Channel 4	DT7000	4				
	÷	0x7250	Analog(SOL) Output Channel 5	DT7000	4				
	÷	0x7260	Analog(SOL) Output Channel 6	DT7000	4				
	±	0x7270	Analog(SOL) Output Channel 7	DT7000	4				
	-	0×8000	Analog Input Setting Ch.0	DT8000	24				
		0x0	SubIndex 000	USINT	1	11	0×B		
		0x1	Enable Limit1	BOOL	0	0	0x0		l
		0x2	Enable Limit2	BOOL	0		0×0		J
		0x9	Range Mode	DT8001EN	1	6	0x6		l
		0xA	Limit1 Value	DINT	4	0	0×0		
		0xB	Limit2 Value	DINT	4	0	0×0		J
	÷	0×8010	Analog Input Setting Ch.1	DT8000	24				
	÷	0×8020	Analog Input Setting Ch.2	DT8000	24				
	±	0×8030	Analog Input Setting Ch.3	DT8000	24				
	÷	0×8040	Analog Input Setting Ch.4	DT8000	24				
	Đ	0×8050	Analog Input Setting Ch.5	DT8000	24				
	Ð	0×8060	Analog Input Setting Ch.6	DT8000	24				
Index Order	÷	0×8070	Analog Input Setting Ch.7	DT8000	24				
	÷	0×8080	Analog Input Setting Ch.8	DT8000	24				1

2. SDO Write



2.		

•

Slave RW		x
02: [0202] AES-CBC-AIO	EEPROM CoE SDO Register Update	
EtherCAT ≟. Net 0	Address	
	Size	
	Value	
	+ Decimal	0
Index Order ID Order	RenewCRC Read	₩rite

3. SDO Write Range Mode

02 : [0202] AES-CBC-AI0 EEPROM CoE SDO Register Update Index 00 : [02FF] MASTER-DC 01 : [0201] AES-CBC-DIO 02 : [0202] AES-CBC-AIO Value 02 : [0202] AES-CBC-AIO 01 · [0201] AES-CBC-AIO Value 02 · [0202] AES-CBC-AIO 02 · [0202] AES-CBC-AIO 02 · [0202] AES-CBC-AIO 02 · [0202] AES-CBC-AIO	Slave RW		-	x
□ EtherCAT □ 00 : [02FF] MASTER-DC □ 01 : [0201] AES-CBC-DIO □ 02 : [0202] AES-CBC-AIO Size Value □ Decimal	02 : [0202] AES-CBC-AIO	EEPROM CoE SDO	Register Update	
-00 : [02FF] MASTER-DC Sub Index -01 : [0201] AES-CBC-DIO Size -02 : [0202] AES-CBC-AIO Value • Decimal 0	EtherCAT ≜ Net 0	Index		
Size 02 : [0202] AES-CBC-AIO Value ■ Decimal 0		Sub Index		
Value Value Comparison of the second sec		Size		
Decimal		Value		
		 Decimal 		0
Index Order	Index Order		Bead	Write

CoE SDO

Slave RW			×
02 : [0202] AES-CBC-AIO	EEPROM CoE SDO	Register Update	
⊟ EtherCAT ⊟ Net Ω	Index	0×8	000
	Sub Index		9
01 : [0201] AES-CBC-DIO	Size		1
	Value		3
	• Decimal	ŧ	3
Index Order ID Order		(Liii) Read	W rite

- Index 0x80n0 (n)
- SubIndex 9 (Range Mode)
- Size 1 (1byte)
- Value RangeMode
- Write

02: [0202] AES-CBC-AIO	EEPROM CoE SDO	Register Update
⊒. EtherCAT ⊨. Net 0	Index	0×8000
	Sub Index	9
	Size	1
	Value	0×6
	 Decimal 	6
	I II	Bead Write

From: http://comizoa.co.kr/info/ - -

Permanent link: http://comizoa.co.kr/info/doku.php?id=tempmanuals:00_ets_ai08_rangemode_change

Last update: 2024/07/08 18:23